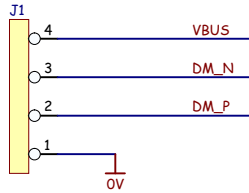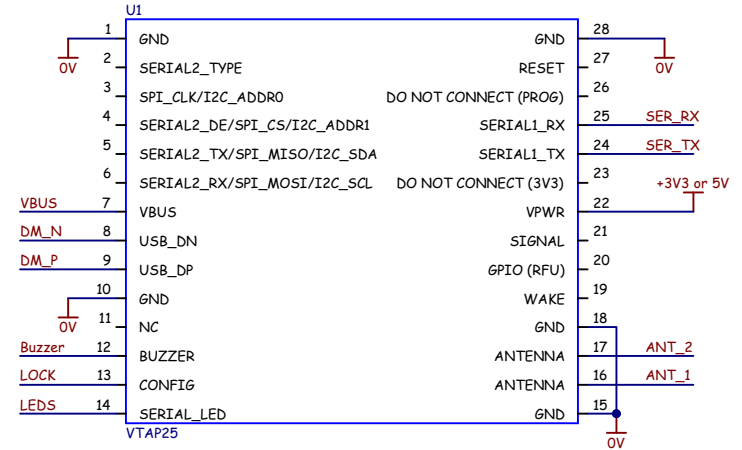**Simple USB Connection**

It is recommended that some form of USB access is implemented even if this is not required functionality to allow debug and simplified production configuration.
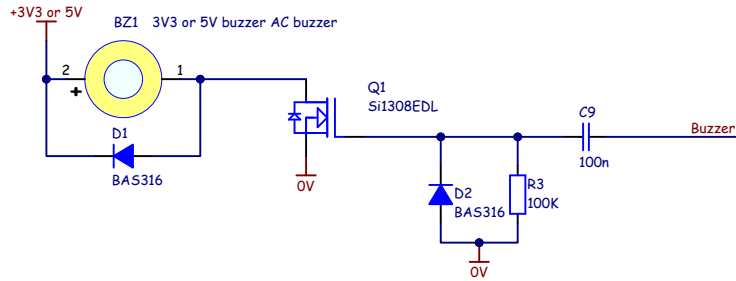
J1
4 — VBUS
3 — DM_N
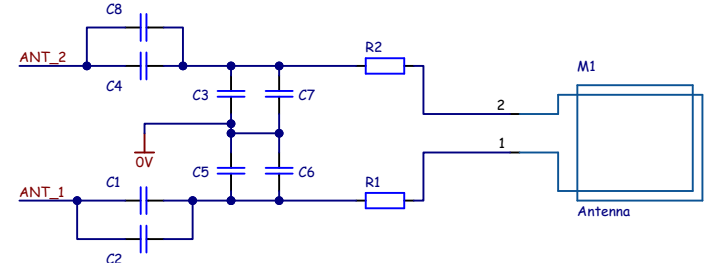2 — DM_P
1 — 0V

VTAP25 Module

RESET does not need to be connected unless external reset control is required as part of user function

U1
1 GND — 0V
2 SERIAL2_TYPE
3 SPI_CLK/I2C_ADDR0
4 SERIAL2_DE/SPI_CS/I2C_ADDR1
5 SERIAL2_TX/SPI_MISO/I2C_SDA
6 SERIAL2_RX/SPI_MOSI/I2C_SCL
VBUS 7 VBUS
DM_N 8 USB_DN
DM_P 9 USB_DP
10 GND — 0V
11 NC
Buzzer 12 BUZZER
LOCK 13 CONFIG
LEDS 14 SERIAL_LED

28 GND — 0V
27 RESET
26 DO NOT CONNECT (PROG)
25 SERIAL1_RX — SER_RX
24 SERIAL1_TX — SER_TX
23 DO NOT CONNECT (3V3)
22 VPWR — +3V3 or 5V
21 SIGNAL
20 GPIO (RFU)
19 WAKE
18 GND
17 ANTENNA — ANT_2
16 ANTENNA — ANT_1
15 GND — 0V

VTAP25

Buzzer Circuit

+3V3 or 5V

BZ1 3V3 or 5V buzzer AC buzzer

D1 BAS316

Q1 Si1308EDL

C9 100n — Buzzer

D2 BAS316

R3 100K

0V

Antenna with Matching Circuit

ANT_2
C8
C4

C3
0V
C5

C7

C6

R2

R1

M1
2
1
Antenna

ANT_1
C1
C2

Title: VTAP25 Host Powered Example (Second Draft)
Variant: [No Variations] -
Assembly Revision:

A4

DRAWING NO
[No Variations]-Scm
Sheet 1
Of 1

REV 2
DATE 12Feb2025
ECN N/A
Drawn Dot Origin Ltd